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UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2815

Examiner: Matthew E. Warren

In Re PATENT APPLICATION Of:

Applicants: Makoto TERUI

Serial No.: 10/722,561

Filed: November 28, 2003

For: Semiconductor chip with passive element in
a wiring region of the chip [as amended]

Docket No.: OKI 383

**PETITION FOR
EXTENSION OF TIME**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the period for replying to the Examiner's Action dated October 5, 2004, be extended for two months under the provisions of 37 C.F.R. § 1.136(b). The applicable extension fee of \$450.00 under the provisions of 37 C.F.R. §1.17(a) is submitted herewith.

Should any additional fees be required please charge the same to our Deposit Account No. 18-0002 and advise us accordingly. This paper is being filed in duplicate.

03/08/2005 SZEMDIE1 00000142 10772561

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450.00 OP

Respectfully submitted,

Nicholas S. Bromer
Nicholas S. Bromer - Reg. No. 33,476
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Customer No. 23995
Telephone: 202-371-8976
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NSB:dj

FEE ENCLOSED: \$450.
Please charge any further
fee to our Deposit Account
No. 18-0002